

FIG. 1

FIRST PRINCIPLE DIAGRAM OF THE INVENTION

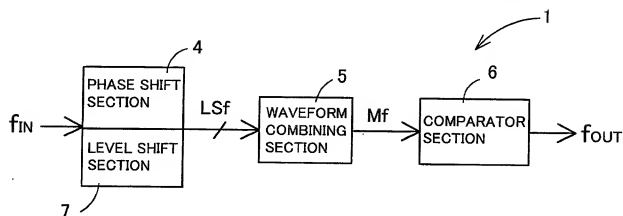


FIG. 2

SECOND PRINCIPLE DIAGRAM OF THE INVENTION

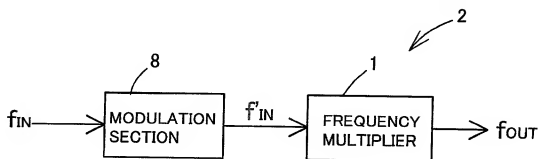


FIG. 3

CIRCUIT BLOCK DIAGRAM OF A FREQUENCY MULTIPLIER
ACCORDING TO A FIRST EMBODIMENT

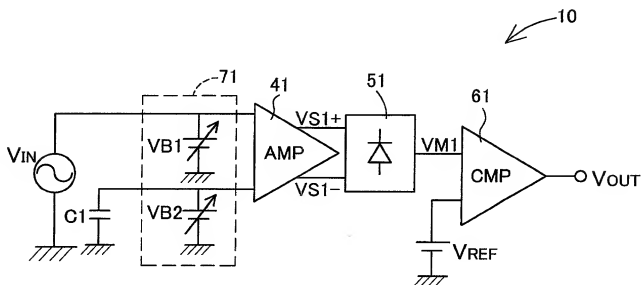


FIG. 4

FREQUENCY-MULTIPLIED WAVEFORM PRODUCED BY THE FREQUENCY MULTIPLIER OF FIG.3

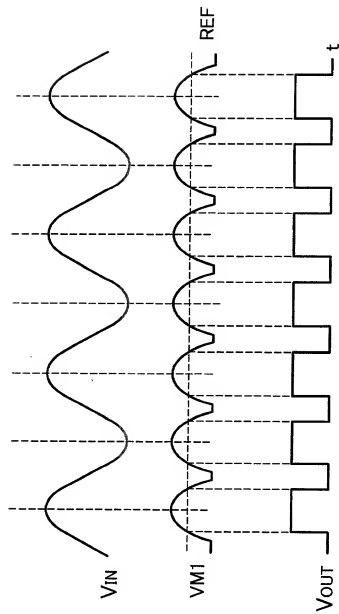
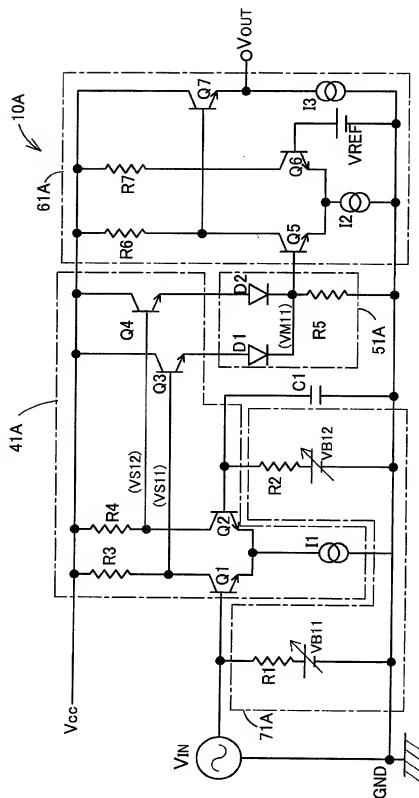


FIG. 5

CIRCUIT DIAGRAM OF A FREQUENCY MULTIPLIER AS A MORE SPECIFIC EXAMPLE ACCORDING TO THE FIRST EMBODIMENT



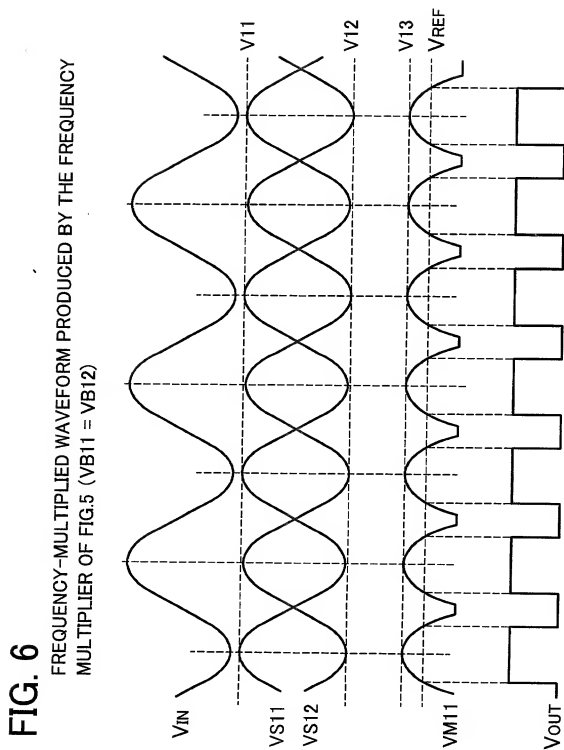


FIG. 7

FREQUENCY-MULTIPLIED WAVEFORM PRODUCED BY THE FREQUENCY MULTIPLIER OF FIG.5 ($V_{B11} < V_{B12}$)

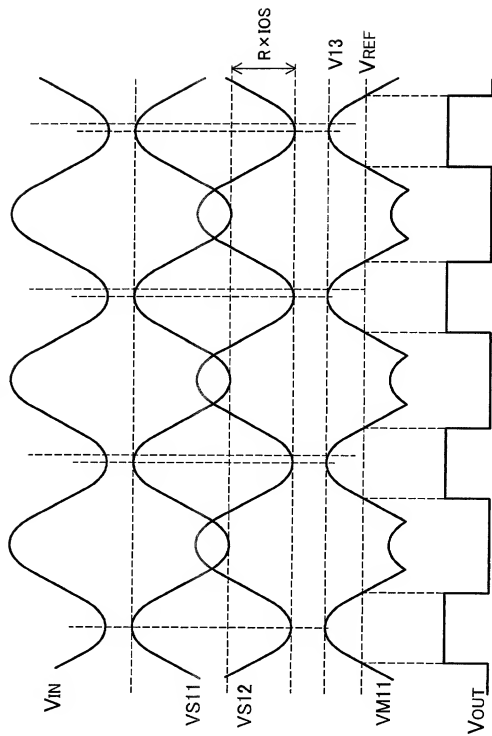


FIG. 8

CIRCUIT BLOCK DIAGRAM OF A FREQUENCY MULTIPLIER ACCORDING TO A SECOND EMBODIMENT

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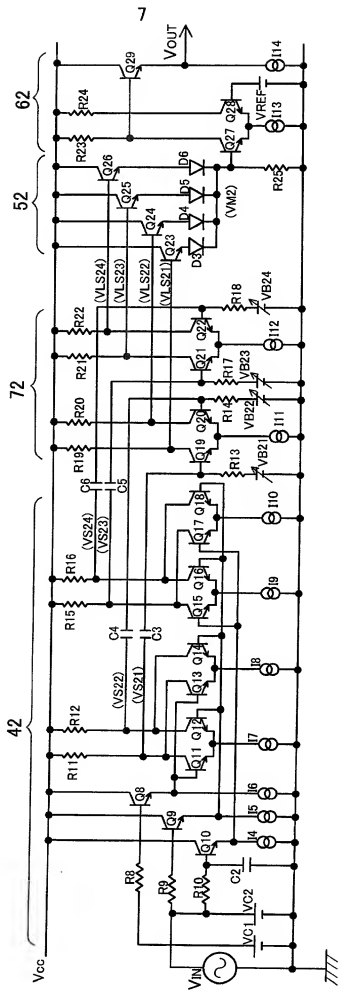


FIG. 9

FREQUENCY—MULTIPLIED WAVEFORM PRODUCED BY THE FREQUENCY MULTIPLIER OF FIG.8
 ($VB21 = VB22 = VB23 = VB24$)

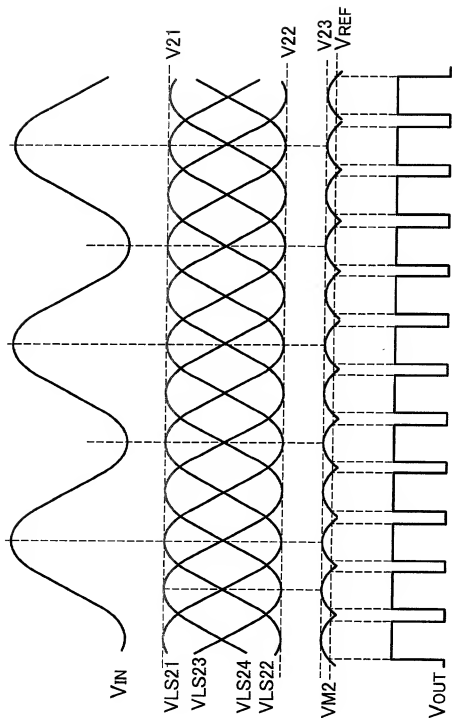


FIG. 10

FREQUENCY- MULTIPLIED WAVEFORM PRODUCED BY THE FREQUENCY MULTIPLIER OF FIG.8
(VB21 = VB22 = VB24 < VB23)

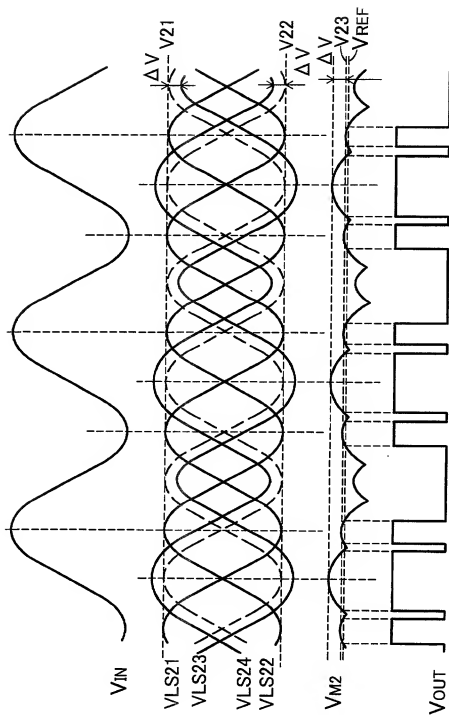


FIG. 11

FREQUENCY- MULTIPLIED WAVEFORM PRODUCED BY THE FREQUENCY MULTIPLIER OF FIG.8
($VB21 = VB24 > VB22 = VB23$)

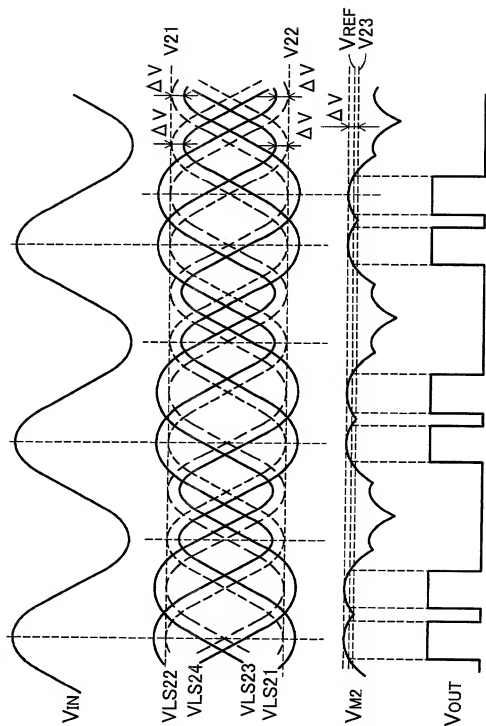


FIG. 12

FREQUENCY—MULTIPLIED WAVEFORM PRODUCED BY THE FREQUENCY MULTIPLIER OF FIG.8
($VB21 = VB23 = VB24 > VB22$)

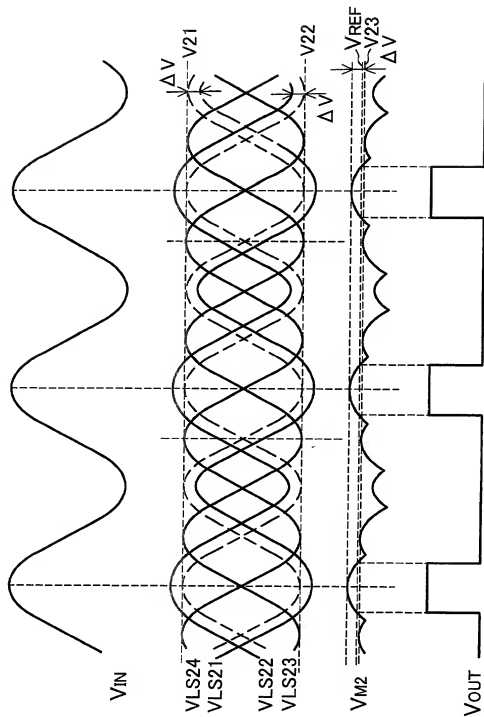


FIG. 13

CIRCUIT BLOCK DIAGRAM OF A FREQUENCY MULTIPLIER
ACCORDING TO A THIRD EMBODIMENT

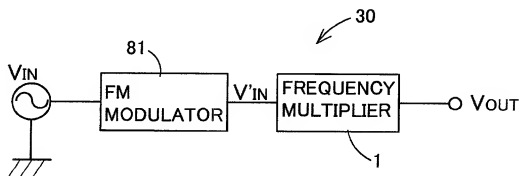


FIG. 14

CIRCUIT BLOCK DIAGRAM OF A FREQUENCY MULTIPLIER
ACCORDING TO A FOURTH EMBODIMENT

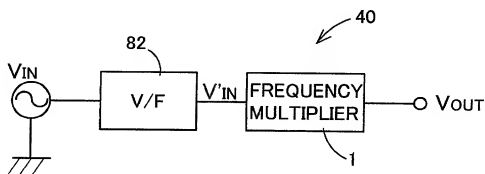
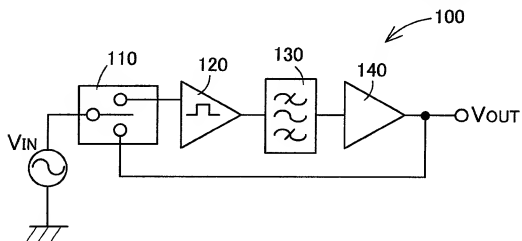


FIG. 15 PRIOR ART

CIRCUIT BLOCK DIAGRAM OF A FREQUENCY MULTIPLIER
ACCORDING TO A FIRST CONVENTIONAL TECHNIQUE

**FIG. 16** PRIOR ART

CIRCUIT BLOCK DIAGRAM OF A FREQUENCY MULTIPLIER
ACCORDING TO A SECOND CONVENTIONAL TECHNIQUE

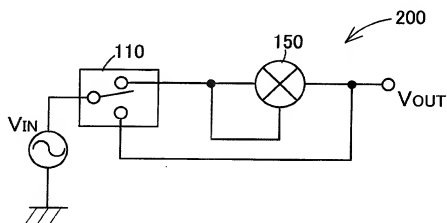


FIG. 17

CIRCUIT DIAGRAM OF A MIXER CIRCUIT (FREQUENCY DOUBLER CIRCUIT) ACCORDING TO THE SECOND CONVENTIONAL TECHNIQUE

